

500mA LDO REGULATOR (Operating Voltage up to 24V)

NO.EA-151-090107

OUTLINE

The R1500x series are CMOS-based positive voltage regulator (VR) ICs. The R1500xxxxB has features of high input voltage operating, 500mA output current drive, and low supply current.

A DMOS transistor is used for the driver, high voltage operating and low on resistance (0.6Ω at $V_{OUT}=10V$) device is realized. A standard regulator circuit with a current limit circuit and a thermal shutdown circuit are built in the R1500x series.

As the operating temperature range is from $-40^{\circ}C$ to $105^{\circ}C$ and maximum input voltage is up to 24V, the R1500x series are suitable for the constant voltage source for car accessories.

The regulator output voltage is fixed in the R1500x. Output voltage accuracy is $\pm 2.0\%$ and output voltage range is from 3.0V to 12.0V with a step of 0.1V. The chip enable pin realizes ultra low supply current standby mode.

Since the packages for these ICs are the SOT-89-5 for high density mounting of the ICs on boards, and the TO-252-5-P2.

*) The DMOS (Double Diffused MOS) transistor adopted by R1500x is characterized by a double diffusion structure which comprises a low density n-type (channel) diffused layer and a high density p-type (sources) diffused layer from the edge of the gate electrode. The R1500x series possess outstanding properties of high operating voltage and low on-resistance, which have been achieved by the channel length scaled down to submicron dimensions and decreased thickness of the gate oxide film.

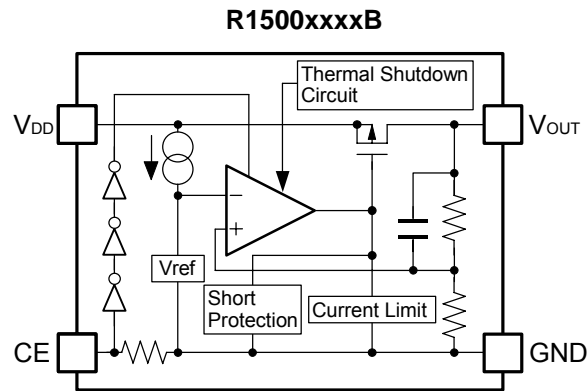
FEATURES

- Input Voltage Range4.0V to 24.0V
- Supply CurrentTyp. $70\mu A$
- Standby CurrentTyp. $0.1\mu A$
- Ripple RejectionTyp. 60dB ($V_{OUT}=5.0V$)
- Temperature-Drift Coefficient of Output VoltageTyp. $\pm 100ppm/^{\circ}C$
- Output Current.....Min. 500mA ($V_{IN}=V_{OUT}+1V$)
- Line RegulationTyp. 0.05%/V
- Output Voltage Accuracy..... $\pm 2\%$
- Packages.....SOT-89-5, TO-252-5-P2
- Output Voltage.....Stepwise setting with a step of 0.1V in the range of 3.0V to 12.0V is possible.
- Built-in Current Limit Circuit
- Built-in Fold-Back Circuit
- Built-in Thermal Shutdown Circuit
- Operating Temperature range $-40^{\circ}C$ to $105^{\circ}C$

APPLICATIONS

- Power source for home appliances such as refrigerators, rice cookers, electric water warmers, etc.
- Power source for car audio equipment, car navigation system, ETC system, etc.
- Power source for notebook PCs, digital TVs, cordless phones, and private LAN system, etc.
- Power source for office equipment machines such as copiers, printers, facsimiles, scanners, projectors, etc.

BLOCK DIAGRAMS



SELECTION GUIDE

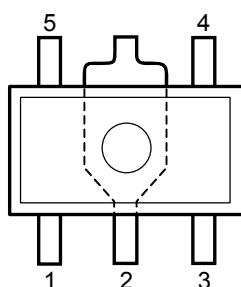
The output voltage, the active type, and the taping type for the ICs can be selected at the user's request. The selection can be made with designating the part number as shown below;

R1500xxxxx-xx-x ← Part Number
 ↑ ↑ ↑ ↑ ↑
 a b c d e

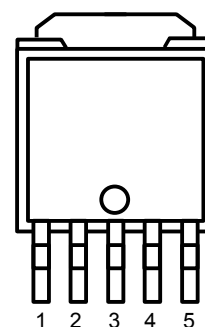
Code	Contents
a	Designation of Package Type: H: SOT-89-5 J: TO-252-5-P2
b	Setting Output Voltage (V_{OUT}): Stepwise setting with a step of 0.1V in the range of 3.0V to 12.0V is possible.
c	Designation of Active Type: B: active high
d	Designation of Taping Type: Ex. T1 (Refer to Taping Specifications)
e	Designation of composition of pin plating: -F: Lead free solder plating (SOT-89-5, TO-252-5-P2)

PIN CONFIGURATIONS

• SOT-89-5



• TO-252-5-P2



PIN DESCRIPTIONS

• SOT-89-5

Pin No.	Symbol	Description
1	V_{DD}	Input Pin
2	GND*	Ground Pin
3	GND*	Ground Pin
4	CE	Chip Enable Pin ("H" Active)
5	V_{OUT}	Output Pin

• TO-252-5-P2

Pin No.	Symbol	Description
1	V_{DD}	Input Pin
2	GND*	Ground Pin
3	GND*	Ground Pin
4	CE	Chip Enable Pin ("H" Active)
5	V_{OUT}	Output Pin

*) No.2 pin and No.3 pin of SOT-89-5 and TO-252-5-P2 package must be wired to the GND plane when it is mounted on board.

ABSOLUTE MAXIMUM RATINGS

Symbol	Item	Rating	Unit
V_{IN}	Input Voltage	36	V
V_{CE}	Input Voltage (CE Pin)	-0.3 to $V_{IN} \leq 36$	V
V_{OUT}	Output Voltage	-0.3 to $V_{IN} \leq 36$	V
I_{OUT}	Output Current	900	mA
P_D	Power Dissipation (SOT-89-5)*	900	mW
	Power Dissipation (TO-252-5-P2)*	1900	
T_{opt}	Operating Temperature Range	-40 to 105	$^{\circ}C$
T_{stg}	Storage Temperature Range	-55 to 125	$^{\circ}C$

*) For Power Dissipation, please refer to PACKAGE INFORMATION to be described.

ABSOLUTE MAXIMUM RATINGS

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the life time and safety for both device and system using the device in the field.

The functional operation at or over these absolute maximum ratings is not assured.

ELECTRICAL CHARACTERISTICS

• R1500xxxxB

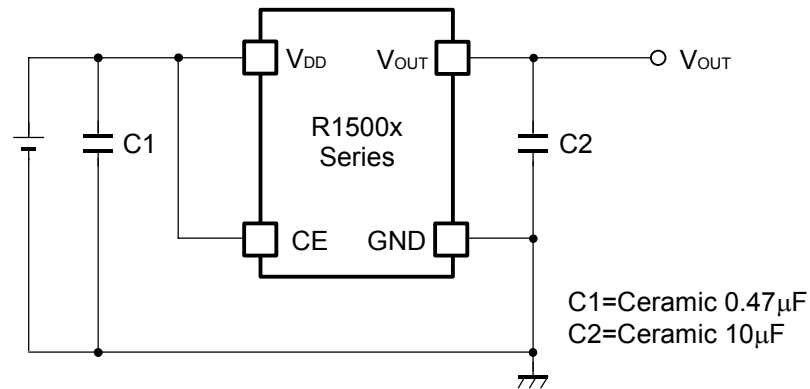
T_{opt}=25°C

Symbol	Item	Conditions	Min.	Typ.	Max.	Unit
V _{IN}	Input Voltage		4		24	V
I _{SS}	Supply Current	V _{IN} =V _{OUT} +1.0V, V _{IN} =V _{CE}		70	130	μA
I _{standby}	Standby Current	V _{IN} =24V		0.1	1.0	μA
V _{OUT}	Output Voltage	V _{IN} =V _{OUT} +1.0V, I _{OUT} =100mA	×0.98		×1.02	V
I _{OUT}	Output Current	V _{IN} =V _{OUT} +1.0V	500			mA
ΔV _{OUT} /ΔI _{OUT}	Load Regulation	V _{IN} =V _{OUT} +2.0V, 0.1mA ≤ I _{OUT} ≤ 200mA		25	60	mV
ΔV _{OUT} /ΔV _{IN}	Line Regulation	V _{OUT} +1V ≤ V _{IN} ≤ 24V, I _{OUT} =10mA		0.05	0.1	%/V
V _{DIF}	Dropout Voltage	I _{OUT} =200mA	3.0V ≤ V _{OUT} < 5.0V	0.135	0.225	V
			5.0V ≤ V _{OUT} < 9.0V	0.115	0.180	
			9.0V ≤ V _{OUT} ≤ 12.0V	0.095	0.155	
ΔV _{OUT} /ΔT _{opt}	Output Voltage Temperature Coefficient	V _{IN} =V _{OUT} +2.0V, I _{OUT} =100μA -40°C ≤ T _{opt} ≤ 105°C		±100		ppm/°C
I _{lim}	Short Current Limit	V _{OUT} =0V		65		mA
RR	Ripple Rejection	f=1kHz, Ripple 0.5Vp-p, I _{OUT} =100mA, V _{IN} =V _{OUT} +2V	V _{OUT} ≤ 6.0V	60		dB
			V _{OUT} > 6.0V	50		
V _{CEH}	CE Input Voltage "H"		2.0		V _{IN}	V
V _{CEL}	CE Input Voltage "L"		0		0.4	V
T _{TSD}	Thermal Shutdown Temperature	Junction Temperature		160		°C
T _{TSR}	Thermal Shutdown Released Temperature	Junction Temperature		135		°C

RECOMMENDED OPERATING CONDITIONS (ELECTRICAL CHARACTERISTICS)

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

TYPICAL APPLICATION



(External Components)

C2: Ceramic 10 μ F MURATA: GRM32DB31E106K (size: 3225)

TECHNICAL NOTES

When using these ICs, consider the following points:

Phase Compensation

In these ICs, phase compensation is made for securing stable operation even if the load current is varied. For this purpose, use a capacitor C2 with good frequency characteristics and ESR (Equivalent Series Resistance).

If you use a tantalum type capacitor and ESR value of the capacitor is large, output might be unstable. Evaluate your circuit with considering frequency characteristics.

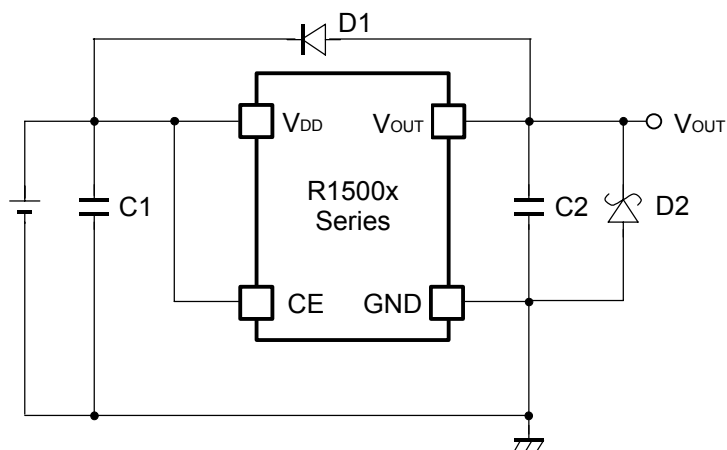
Depending on the capacitor size, manufacturer, and part number, the bias characteristics and temperature characteristics are different. Evaluate the circuit with actual using capacitors.

PCB Layout

Make V_{DD} and GND lines sufficient. If their impedance is high, noise pickup or unstable operation may result. Connect a capacitor C1 with a capacitance value as much as 0.47 μ F or more between V_{DD} and GND pin, and as close as possible to the pins.

Set external components, especially the output capacitor C2, as close as possible to the ICs, and make wiring as short as possible.

No.2 pin and No.3 pin of SOT-89-5 and TO-252-5-P2 package must be wired to the GND plane when it is mounted on board.

TYPICAL APPLICATION FOR PREVENTING IC DESTRUCTION

C1: 0.47 μ F or more (preventing for unstable operation)

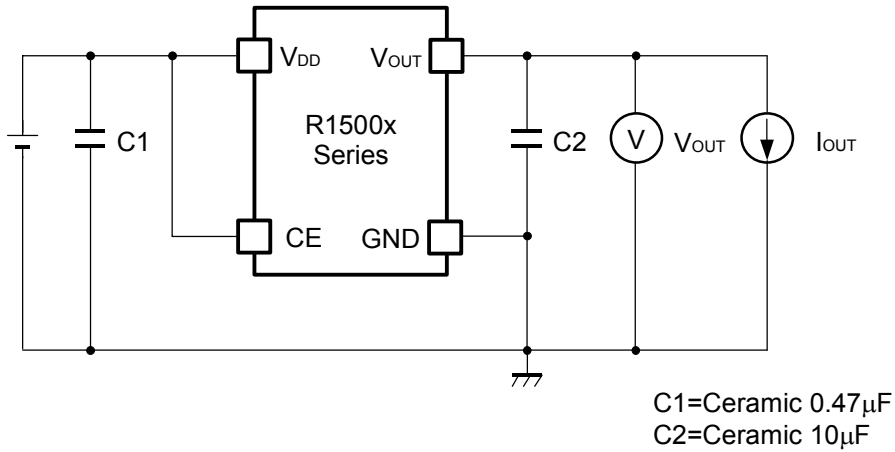
C2: 10 μ F or more (preventing for unstable operation)

D1: If V_{OUT} pin could be higher than V_{IN} pin, D1 is necessary.

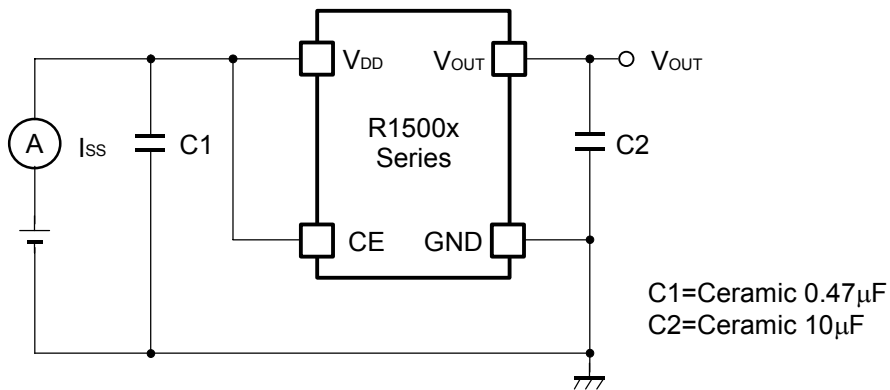
D2: If V_{OUT} pin could be lower than GND pin, SBD is necessary.

Note: Do not force the voltage to V_{OUT} pin.

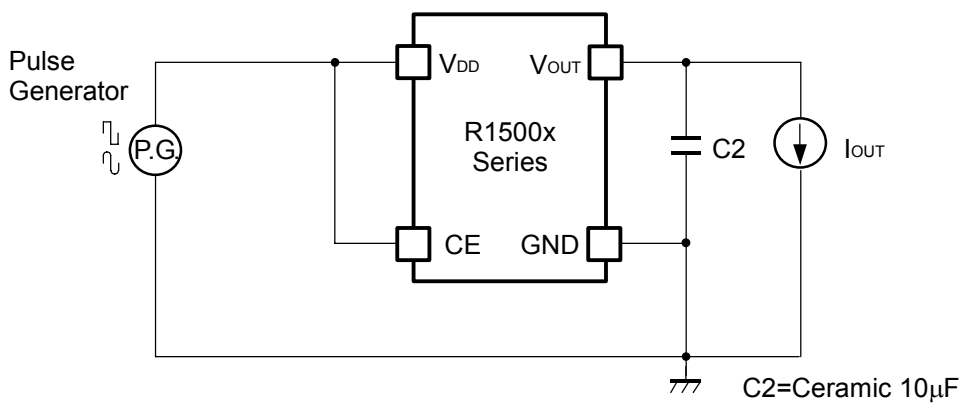
TEST CIRCUITS



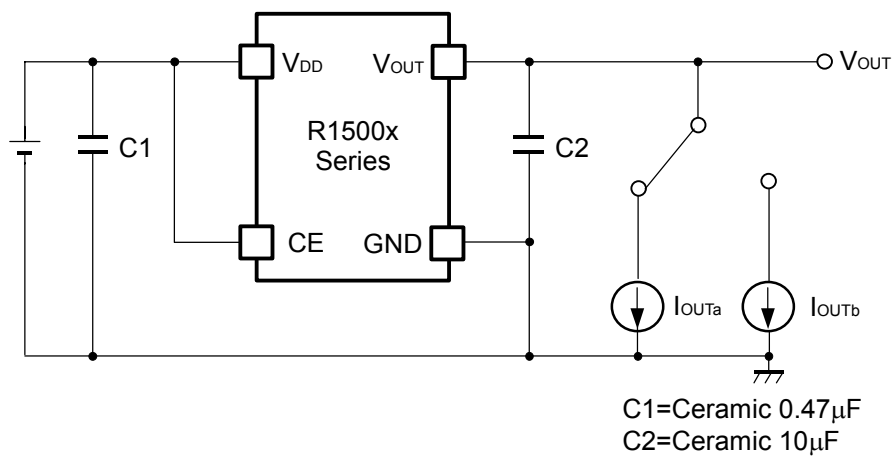
Basic Test Circuit



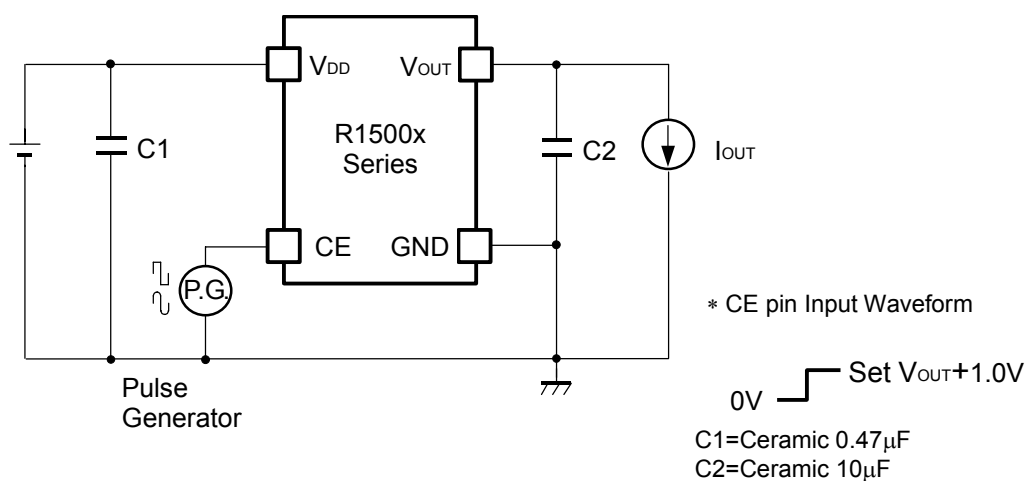
Test Circuit for Supply Current



Test Circuit for Ripple Rejection, Input Transient Response



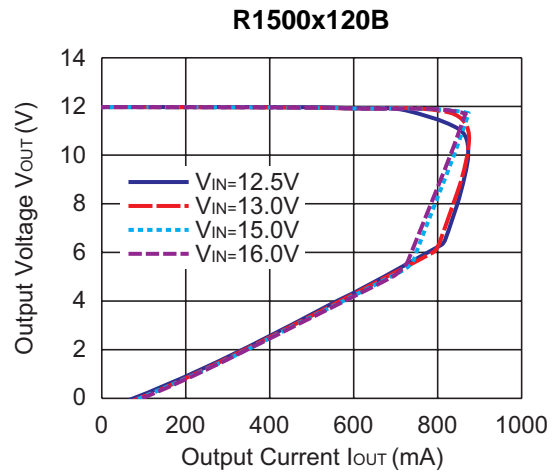
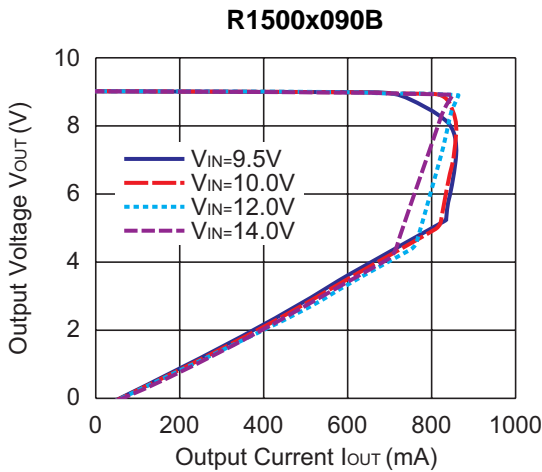
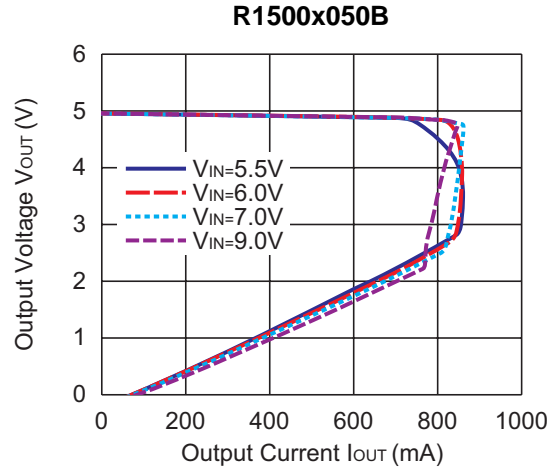
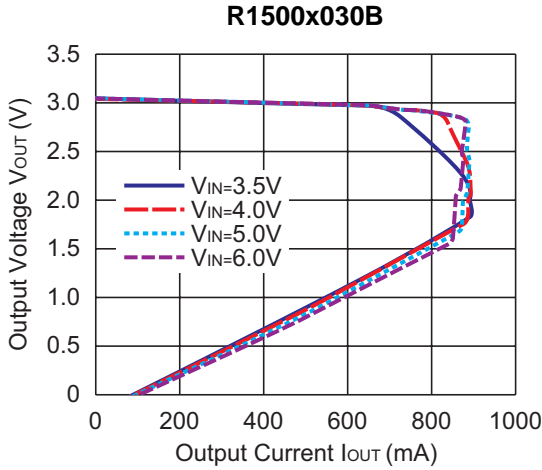
Test Circuit for Load Transient Response



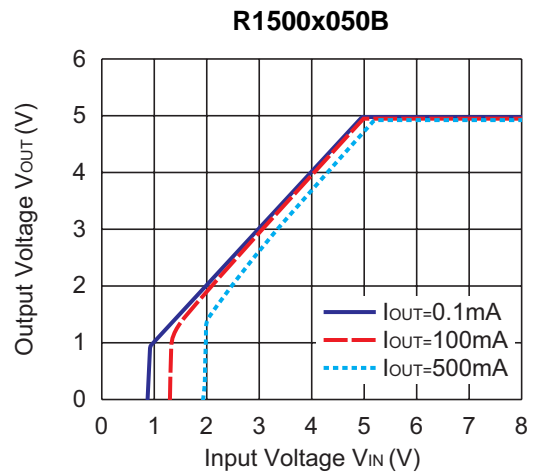
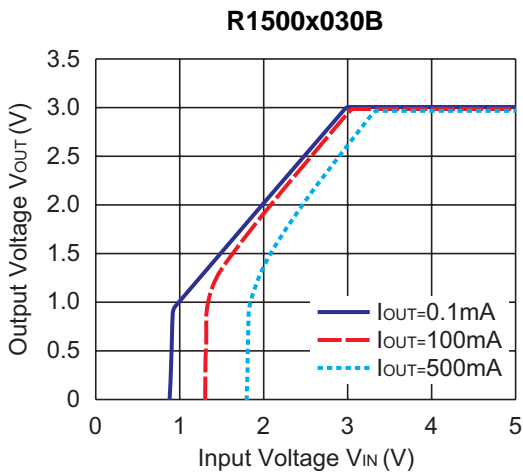
Test Circuit for Turn On Speed with CE pin

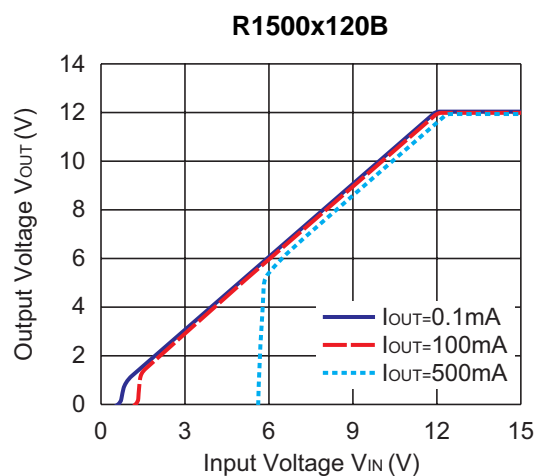
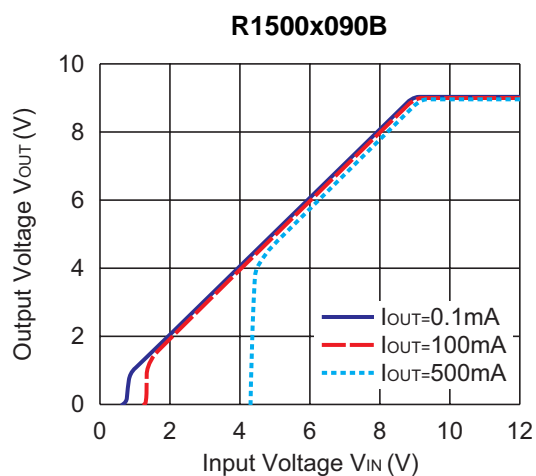
TYPICAL CHARACTERISTICS

1) Output Voltage vs. Output Current (C1=Ceramic 0.47 μ F, C2=Ceramic 10 μ F, Topt=25°C)

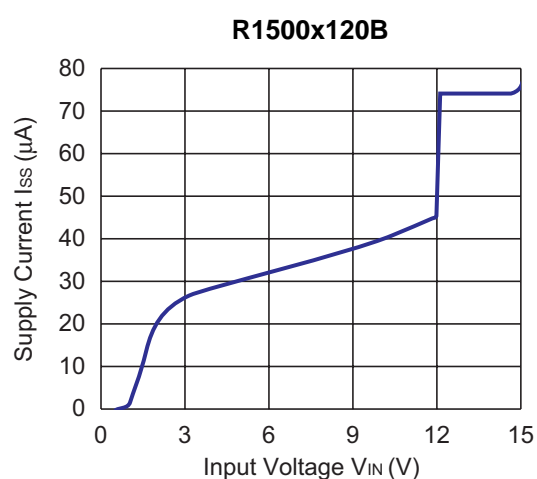
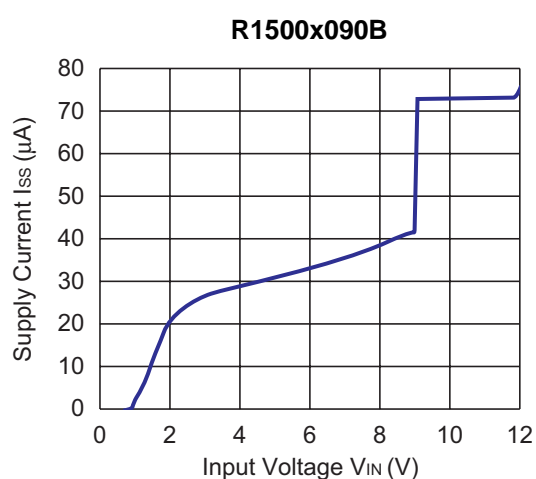
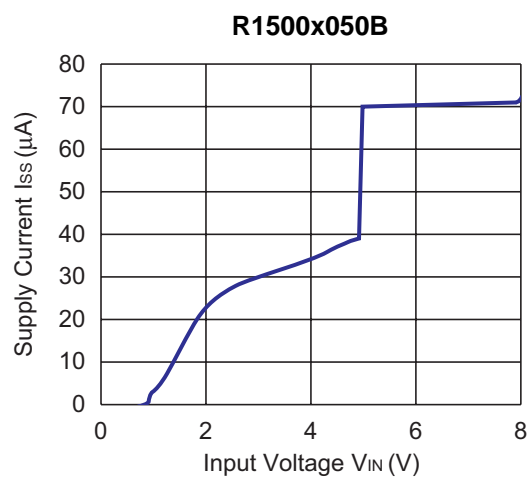
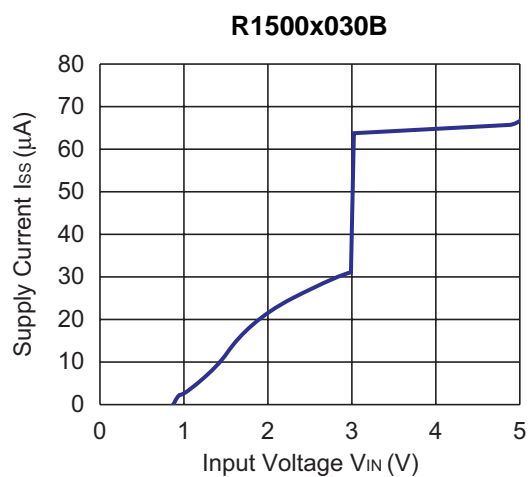


2) Output Voltage vs. Input Voltage (C1=Ceramic 0.47 μ F, C2=Ceramic 10 μ F, Topt=25°C)

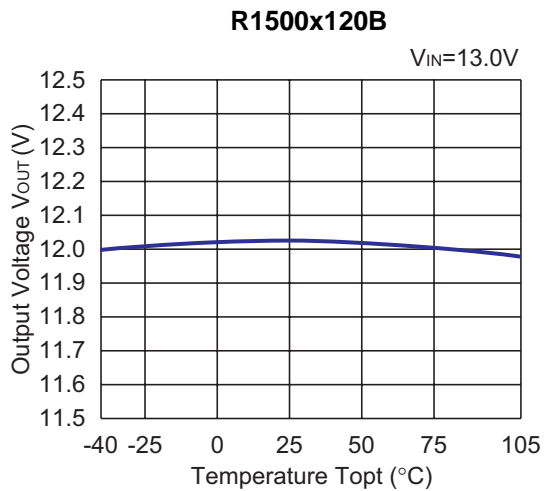
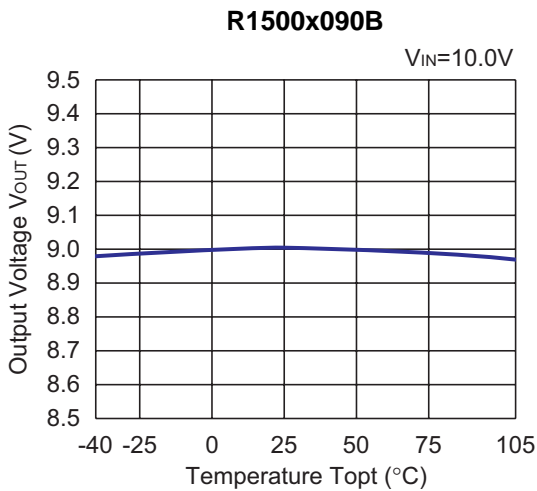
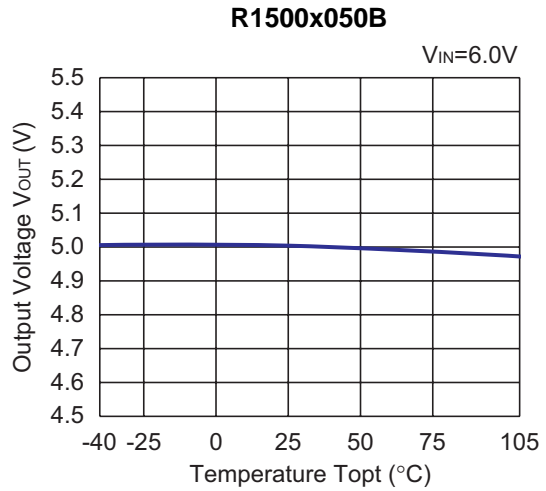
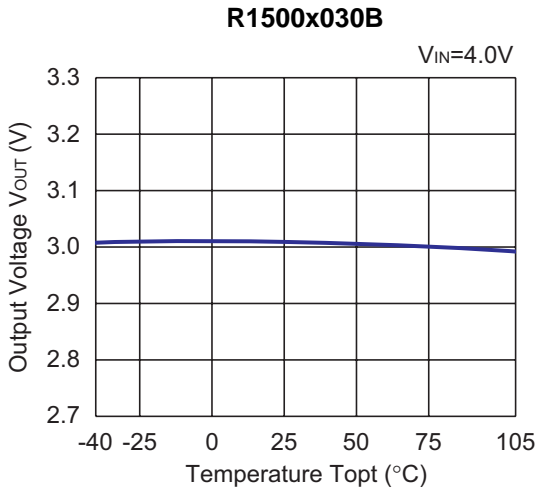




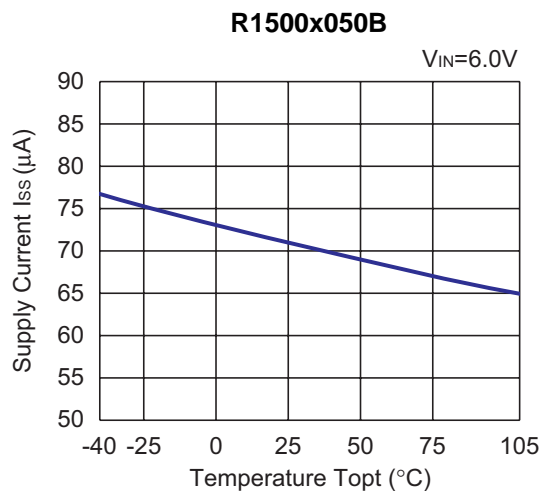
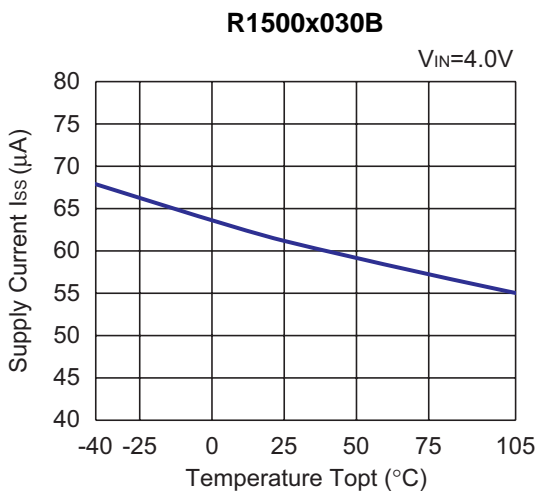
3) Supply Current vs. Input Voltage ($C1$ =Ceramic $0.47\mu\text{F}$, $C2$ =Ceramic $10\mu\text{F}$, $T_{opt}=25^\circ\text{C}$)



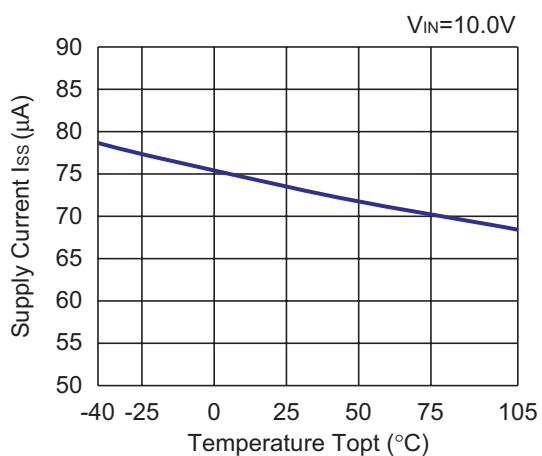
4) Output Voltage vs. Temperature (C1=Ceramic 0.47 μ F, C2=Ceramic 10 μ F, I_{OUT}=100mA)



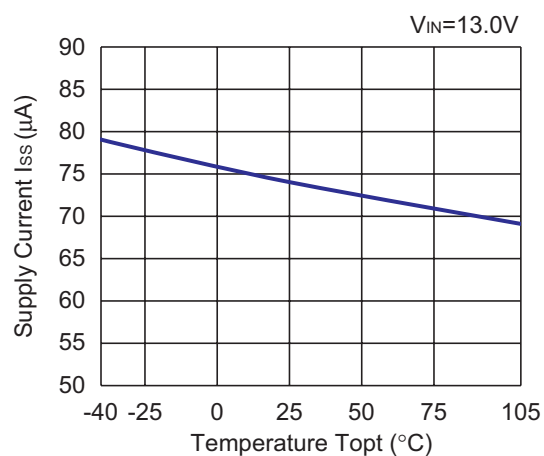
5) Supply Current vs. Temperature (C1=Ceramic 0.47 μ F, C2=Ceramic 10 μ F, I_{OUT}=0mA)



R1500x090B

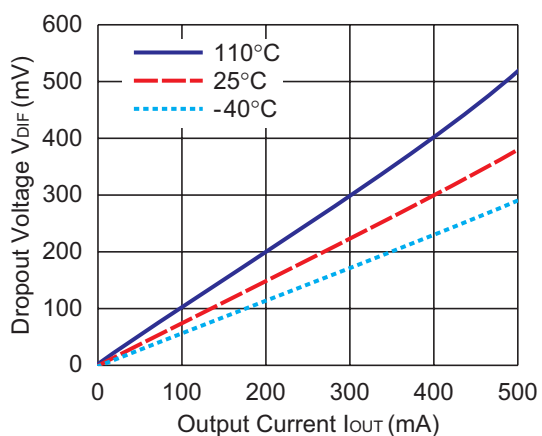


R1500x120B

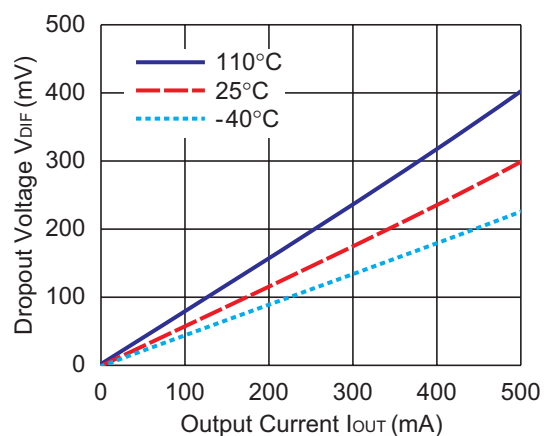


6) Dropout Voltage vs. Output Current ($C1=Ceramic\ 0.47\mu F$, $C2=Ceramic\ 10\mu F$)

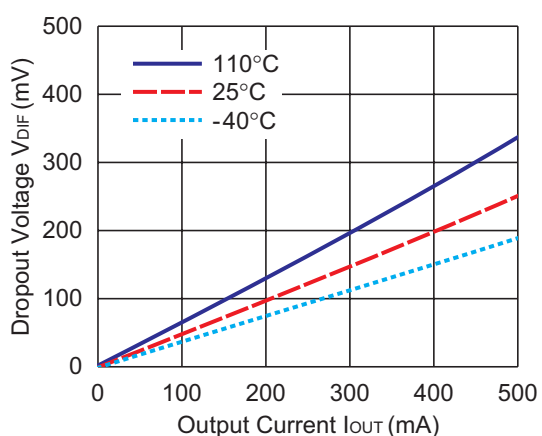
R1500x030B



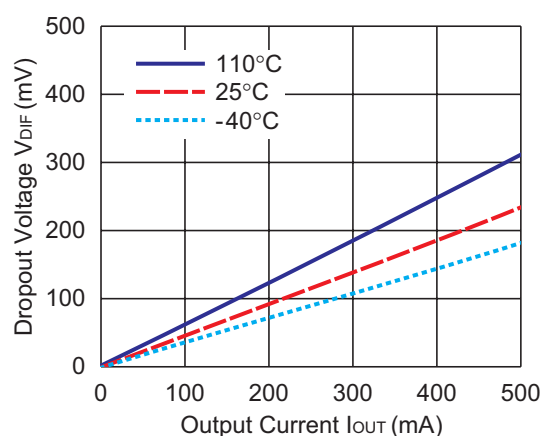
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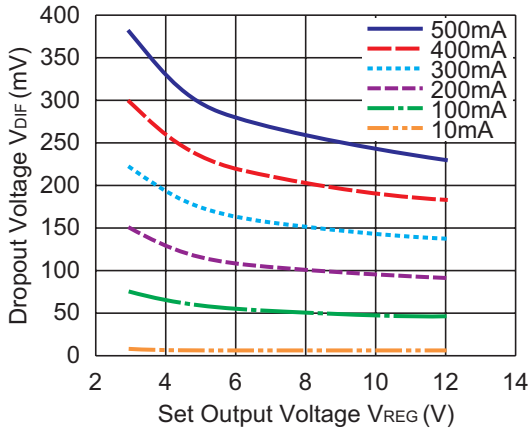
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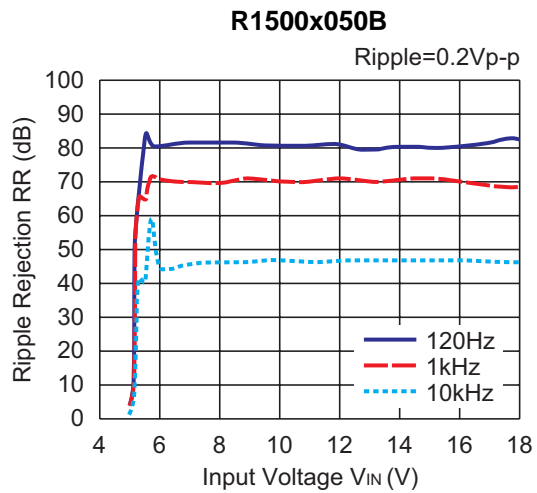
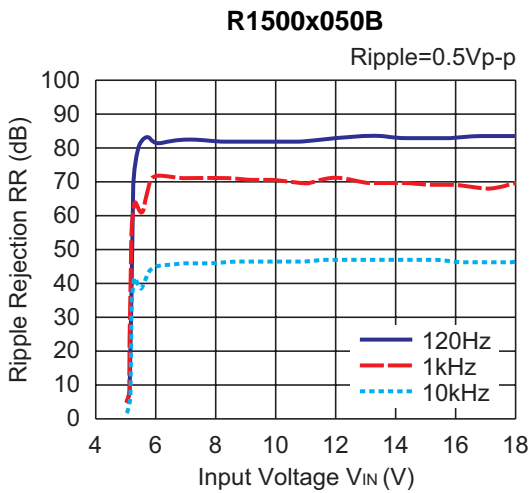
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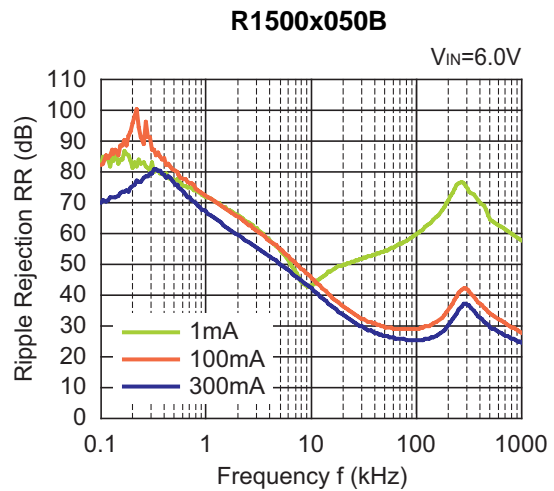
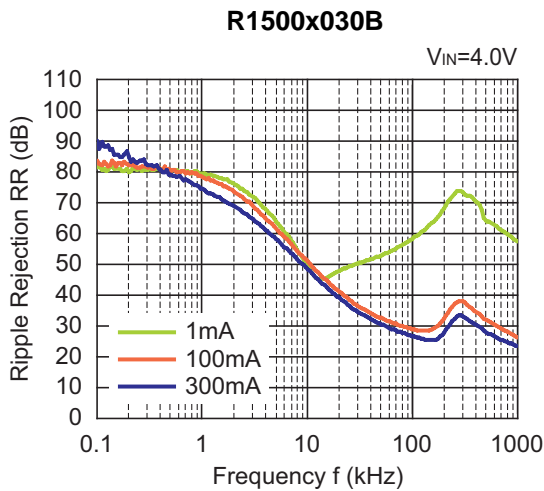
7) Dropout Voltage vs. Set Output Voltage (C1=Ceramic 0.47 μ F, C2=Ceramic 10 μ F, T_{opt}=25°C)

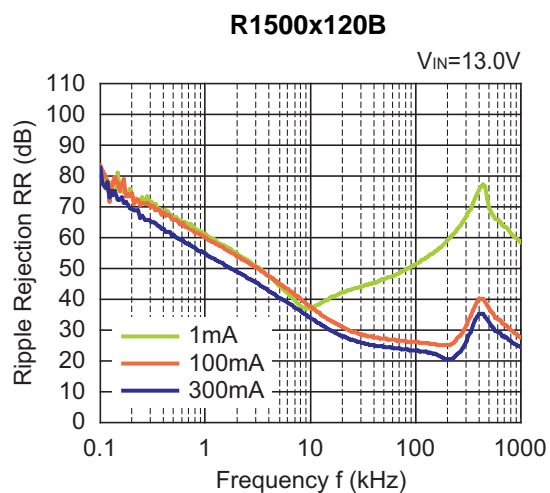
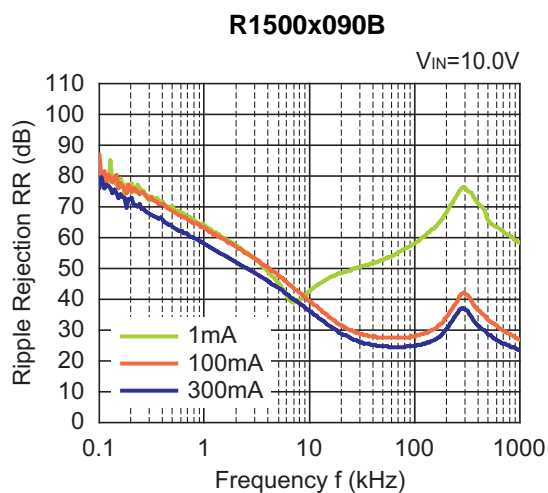


8) Ripple Rejection vs. Input Bias Voltage (C1=none, C2=Ceramic 10 μ F, I_{OUT}=100mA, T_{opt}=25°C)

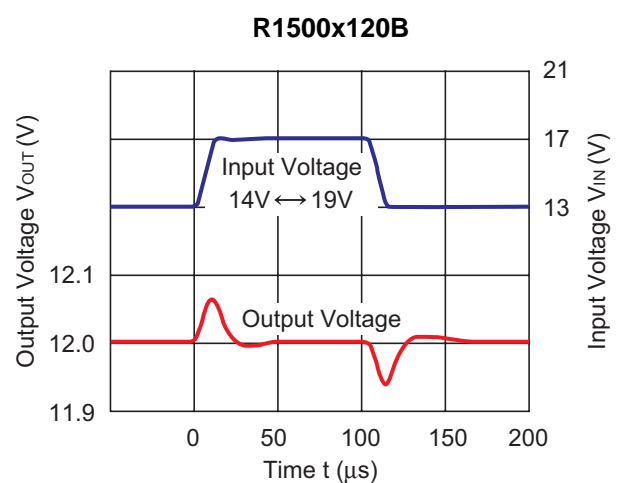
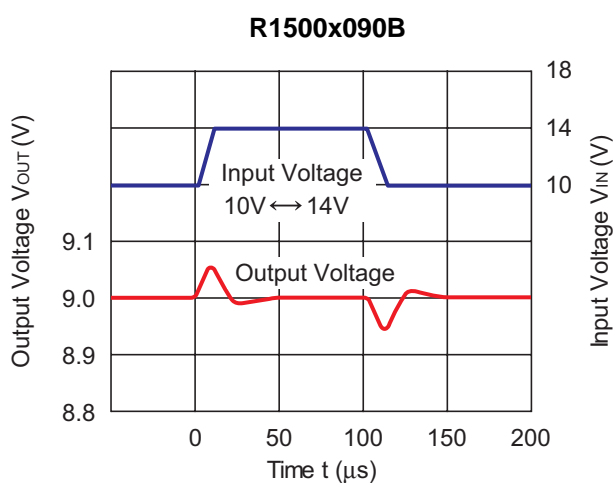
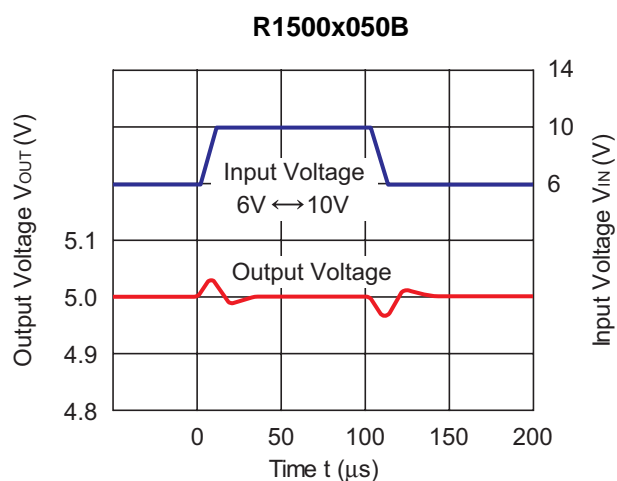
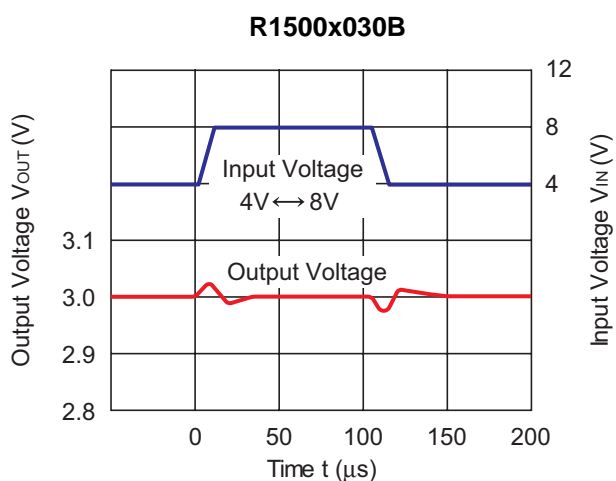


9) Ripple Rejection vs. Frequency (C1=none, C2=Ceramic 10 μ F, Ripple=0.5V_{p-p})

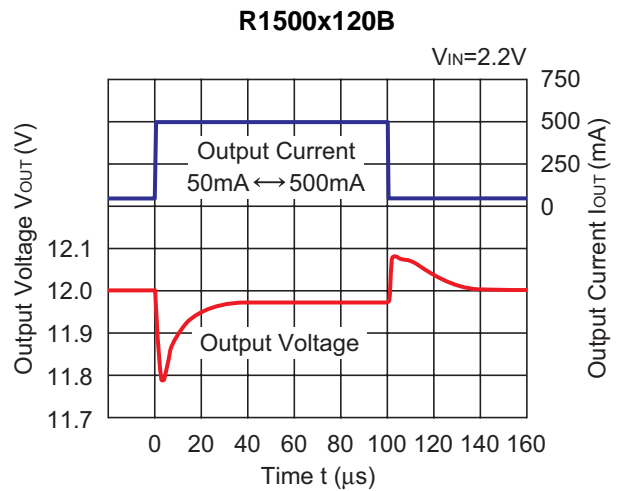
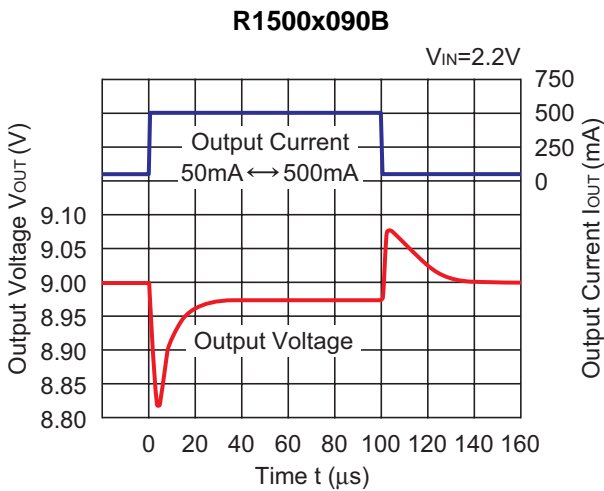
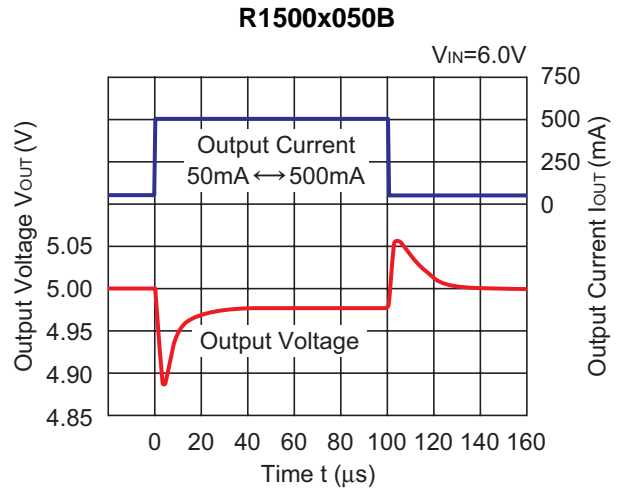
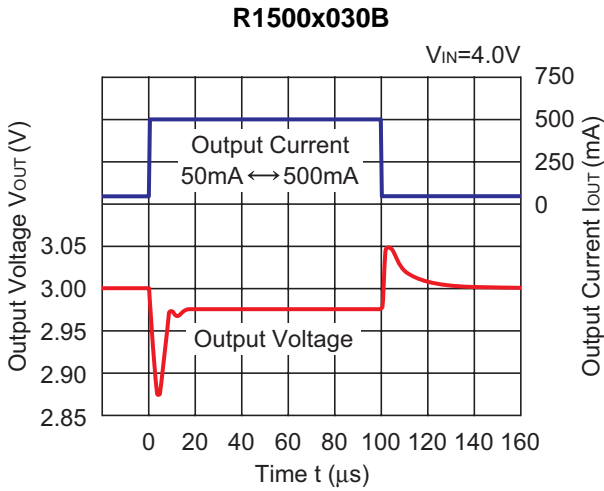




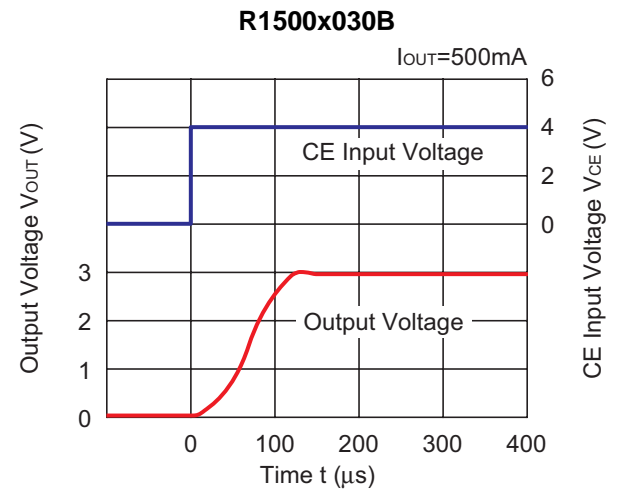
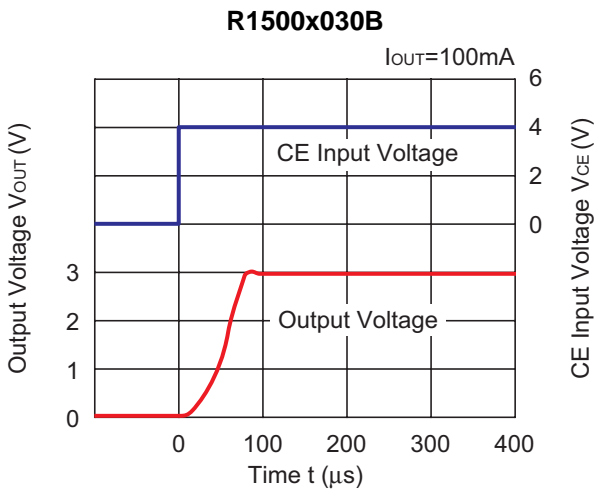
10) Input Transient Response ($C1$ =none, $C2$ =Ceramic $10\mu F$, $I_{OUT}=100mA$, $t_r=t_f=10\mu s$, $T_{opt}=25^\circ C$)



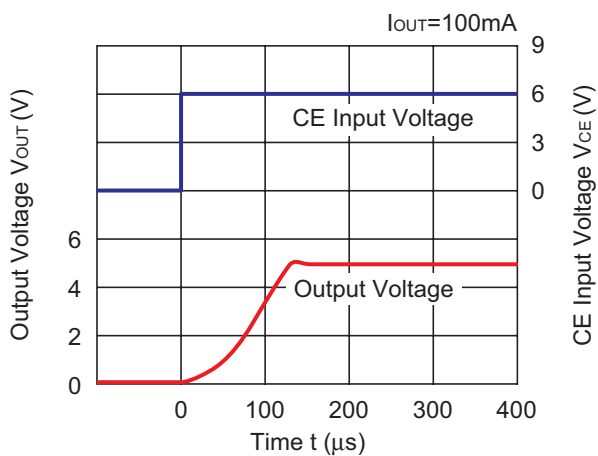
11) Load Transient Response (C1=Ceramic 0.47μF, C2=Ceramic 10μF, tr=tf=0.5μs, Topt=25°C)



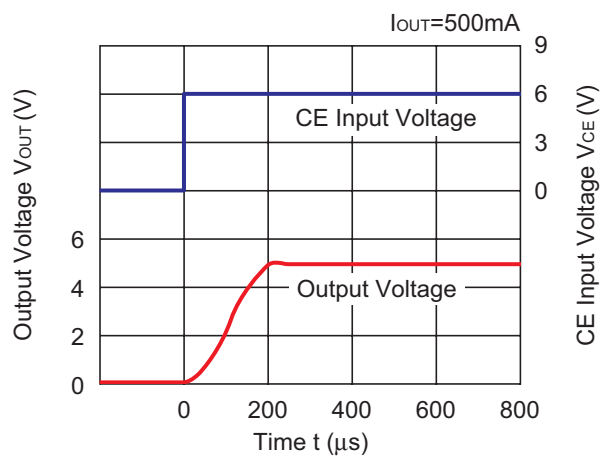
12) Turn On Speed with CE pin (C1=Ceramic 0.47μF, C2=Ceramic 10μF, Topt=25°C)



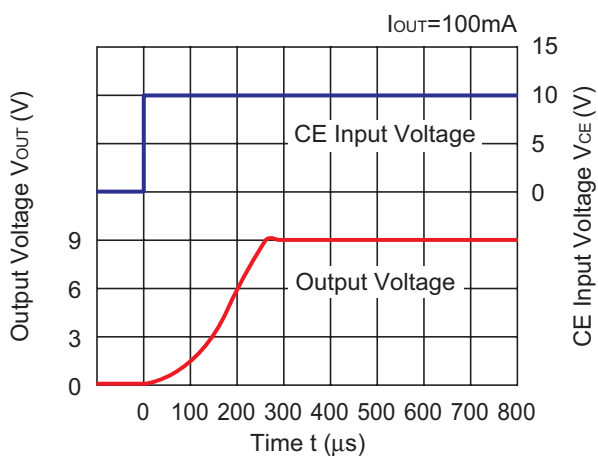
R1500x050B



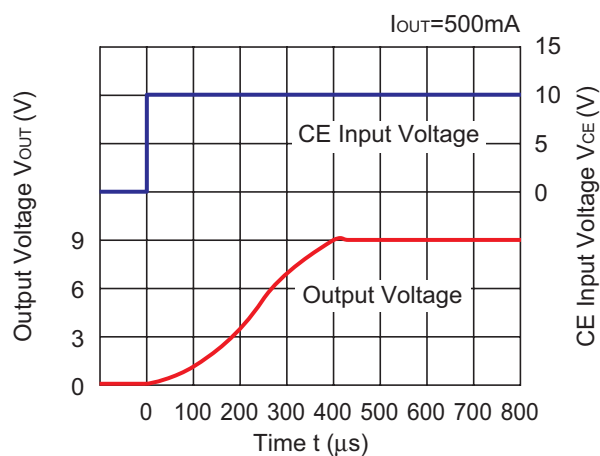
R1500x050B



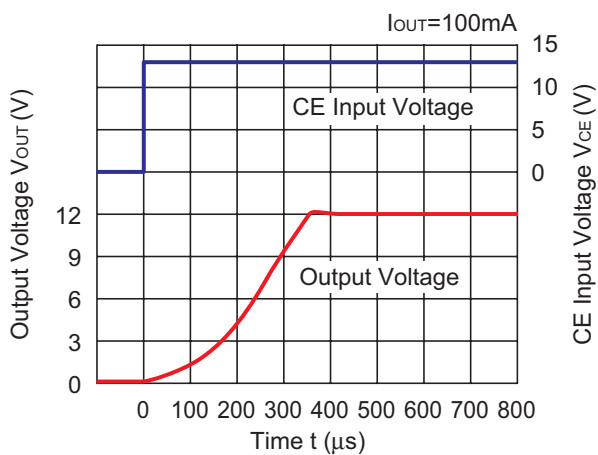
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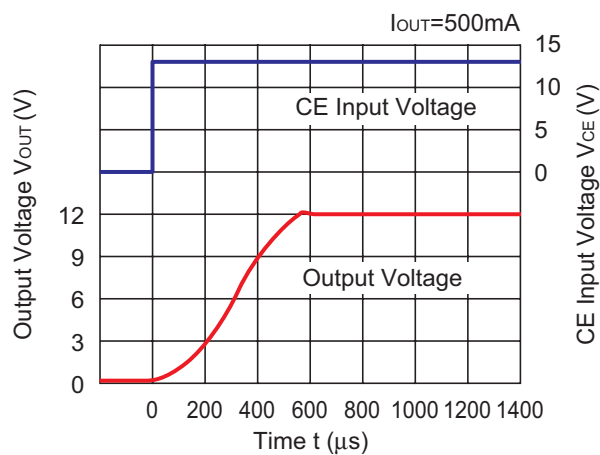
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R1500x120B

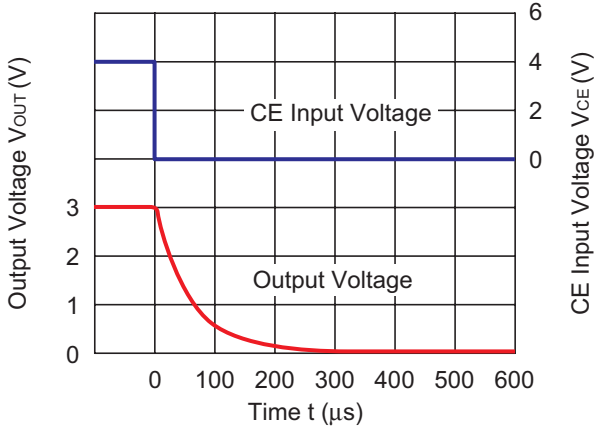


R1500x120B

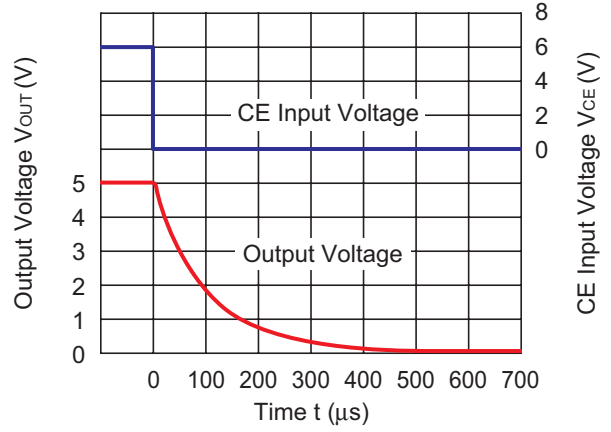


13) Turn Off Speed with CE (C1=Ceramic 0.47μF, C2=Ceramic 10μF, I_{OUT}=500mA, T_{opt}=25°C)

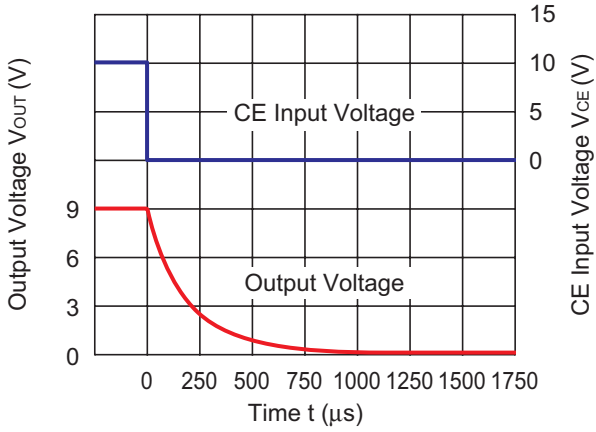
R1500x030B



R1500x050B



R1500x090B



R1500x120B

